Please replace the paragraph at page 4, lines 11-16 with the following new paragraph:

A2

The analog system architecture can be generally referred to as a programmable analog "system-on-a-chip" block. Such programmable blocks can be used in those applications that typically require multiple chips that may be fabricated using different technologies. Implementation in embedded applications, including audio, wireless, handheld, data communications, Internet control, and industrial and consumer systems, is contemplated.

Please replace the paragraph at page 7, lines 3-15 with the following new paragraph:

A3

The preferred embodiment of the present invention, a programmable analog system architecture, is thus described. While the present invention has been described in particular embodiments, it should be appreciated that the present invention should not be construed as limited by such embodiments, but rather construed according to the following claims.

SUPPORT FOR AMENDMENTS

Support for the amendments herein can be found throughout the specification (e.g., page 30, lines 14-20), Title and Abstract as originally filed. The present amendment intends to remove references to the trademarks of Cypress MicroSystems, Inc. (see, e.g., M.P.E.P. § 608.01(v) and the attached printouts from http://www.cypressmicro.com/corporate/ CY_Announces_nov_13_2000.html and http://tess.uspto.gov/, notably the "PSOC" trademark registration information therein.) No new matter is introduced.